

**REMARKS**

Reconsideration and further examination of this application is respectfully requested. Claims 1-17 were previously indicated to be allowable. Claims 1, 3-9, 11-13, 15 and 17 were objected to for various informalities in the last office action. Applicant has amended claims 1, 3-15 and 17 to correct these informalities. Claims 1, 2 and 7-17 have been allowed. Allowance of these claims is greatly appreciated.

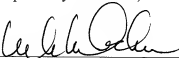
The interview with Examiner Lawrence B. Williams on November 29, 2006 was greatly appreciated. In that interview, Applicant's attorney, William W. Cochran, discussed the rejection of claims 3-6 over the Yoshizawa reference. Applicant's attorney pointed out the differences between the Yoshizawa reference and claims 3-6. In response to that interview, Applicant has amended claim 3 to include the terminology "whenever said block or signal component is detected in said input signal" to better distinguish claim 3 over Yoshizawa." This limitation further clarifies the difference of claim 3, in which the active filter is applied to the input signal in response to the control signal "whenever said blocker signal component is detected in said input signal." As pointed out by Applicant, the active filter circuit 12 is always applied to the output of the variable gain circuit 11, which is clearly different from the limitations of claim 3.

This amendment now places this application in condition for allowance, and such action is earnestly solicited.

Dated this 4<sup>th</sup> day of December, 2006.

Respectfully submitted,

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